

## **AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions, and listing, of claims in the application.

### **Listing of Claims:**

1. (Currently Amended) A liquid crystal display device comprising:  
a first substrate including a transparent substrate, a plurality of unit cells and a first transparent electrode, each of the unit cells having i) a sensor thin film transistor for receiving a light reflected from a fingerprint to generate electric charges corresponding to an intensity of the reflected light, ii) a storage device for storing the electric charges, iii) a first switch thin film transistor for receiving the electric charges from the storage device to output the electric charges in response to an external control signal,

wherein

the transparent substrate includes an upper surface, a lower surface facing the upper surface, and sidewalls connecting the upper and lower surfaces; and

the first transparent electrode is being disposed on the lower surface of the first substrate, the first transparent electrode makes contact with the lower surface of the first substrate, and the unit cells make contact with the upper surface of the lower surface opposing a surface including the unit cells, wherein no additional substrate for forming the first transparent electrode is provided;

a second substrate including a pixel, the pixel having i) a second switch thin film transistor, ii) a data line electrically coupled with a first electrode of the second switch thin film transistor, iii) a gate line electrically coupled with a second electrode of the second switch thin film transistor, iv) a color filter layer formed on first portions of the gate line, the data line and the second switch thin film transistor, v) a second transparent electrode formed on the color filter layer and electrically coupled with a second portion of the first electrode; and

a liquid crystal layer interposed between the first and second substrates, wherein the liquid crystal layer contacts the first transparent electrode disposed on the lower surface of the first substrate.

2. (Previously Presented) The liquid crystal display device of claim 1, wherein the second substrate further comprises a first insulation layer disposed between the color filter layer and the second transparent electrode to cover the color filter layer, the second transparent electrode electrically coupled with the second portion of the first electrode.

3. (Original) The liquid crystal display device of claim 1, wherein at least two unit cells is arranged over one pixel.

4. (Previously Presented) The liquid crystal display device of claim 3, wherein three unit cells are arranged over the pixel having a first aspect ratio of 1:3, each of the unit cells having a second aspect ratio of 1:1.

5. (Original) The liquid crystal display device of claim 4, wherein the first substrate further comprises a sensor signal output line for outputting a sensing signal, the sensor signal output line connected with the sensor thin film transistor, the gate line and a third electrode of the sensor thin film transistor, and the sensor signal output line comprised of transparent conductive material.

6. (Original) The liquid crystal display device of claim 1, wherein the first substrate further comprises a second insulation layer covering the sensor thin film transistor, the storage device and the first switch thin film transistor.

7. (Original) The liquid crystal display device of claim 6, wherein the first switching thin film transistor comprises:

a channel region comprised of amorphous silicon; and

a light shielding layer, disposed on a third portion of the second insulation layer, for shielding the channel region from the light reflected from the fingerprint, the third portion disposed over the channel region, and the light shielding layer comprised of Cr/CrxOy.

8. (Original) The liquid crystal device of claim 6, wherein the first switching thin film transistor comprises:

a channel region comprised of amorphous silicon; and

a light shielding layer, disposed over the channel region, for shielding the channel region from the light reflected from the fingerprint, and the light shielding layer comprised of Cr/CrxOy.

9. (Original) The liquid crystal display device of claim 1, wherein the liquid crystal display device further comprises:

a data reading part, disposed adjacent to a first side face of the first substrate to be connected to the first side face of the first substrate, for receiving the charges from a first electrode of the first switch thin film transistor to generate a fingerprint identification signal corresponding to the fingerprint;

a first gate driver part, disposed adjacent to a second side face of the first substrate to be connected to the second side face of the first substrate, for turning on or turning off a second electrode of the first switch thin film transistor and a second electrode of the sensor thin film transistor;

a data driver part, disposed adjacent to a first side face of the second substrate to be connected to the first side face of the second substrate, for applying an image data signal to a third electrode of the second thin film transistor through the data line; and

a second gate driver part, disposed adjacent to a second side face of the second substrate to be connected to the second side face of the second substrate, for turning on or turning off the second electrode of the second switch thin film transistor, wherein the data driver part is opposite to the data reading part and the first gate driver part is opposite to the second gate driver part.

10. (Currently Amended) A method of manufacturing a liquid crystal display device, the method comprising:

forming a sensor thin film transistor, a storage device and a first switch thin film transistor on a first transparent substrate comprised of insulation material, the sensor thin film transistor receiving a light reflected from a fingerprint to generate electric charges corresponding to an intensity of the reflected light, the storage device storing the electric charges, and the first

switch thin film transistor receiving the electric charges from the storage device to output the electric charges in response to an external control signal;

forming a first transparent electrode on a lower surface of the first transparent substrate including the sensor thin film transistor formed thereon, thereby forming a first substrate including the first transparent substrate, the sensor thin film transistor, the storage device, the first switch thin film transistor and the first transparent electrode, the first transparent electrode making contact with the lower surface of the first transparent substrate, the lower surface opposing a surface including the sensor thin film transistor, the storage device and the first switch thin film transistor, wherein no additional substrate for forming the first transparent electrode is provided;

forming a second switch thin film transistor, a data line and a gate line on a second substrate comprised of insulation material, the data line electrically coupled with a first electrode of the second switch thin film transistor, and the gate line electrically coupled with a second electrode of the second switch thin film transistor,

forming a color filter layer on first portions of the gate line, the data line and the second switch thin film transistor;

forming a second transparent electrode on the color filter layer, the second transparent electrode electrically coupled with a second portion of the first electrode of the second switch thin film transistor; and

forming a liquid crystal layer between the first and second substrates, wherein the liquid crystal layer makes contact with the first transparent electrode formed on the lower surface of the first transparent substrate.

11. (Previously Presented) The method of manufacturing the liquid crystal display device of claim 10, wherein the method further comprises forming a first insulation layer between the color filter layer and the second transparent electrode to cover the color filter layer, the second transparent electrode electrically coupled with the second portion of the first electrode.

12. (Original) The method of manufacturing the liquid crystal display device of claim 10, wherein the method further comprises forming a second insulation layer on the sensor thin

film transistor, the storage device and the first switch thin film transistor, the second insulation layer covering the sensor thin film transistor, the storage device and the first switch thin film transistor.

13. (Original) The method of manufacturing the liquid crystal display device of claim 12, wherein the method further comprises:

forming a channel region comprised of amorphous silicon; and

forming a light shielding layer at a third portion of the second insulation layer, the third portion disposed over the channel region, the light shielding layer shielding the channel region from the light reflected from the fingerprint, and the light shielding layer comprised of Cr/CrxOy.

14. (Previously Presented) The method of manufacturing the liquid crystal display device of claim 12, wherein the method further comprises:

forming a channel region comprised of amorphous silicon; and

forming a light shielding layer, disposed over the channel region, for shielding the channel region from the light reflected from the fingerprint, and the light shielding layer comprised of Cr/CrxOy.

15. (Withdrawn) A method of manufacturing the liquid crystal display device, the method comprising:

forming a sensor thin film transistor, a storage device and a first switch thin film transistor on a first substrate comprised of insulation material, the sensor thin film transistor receiving a light reflected from a fingerprint to generate electric charges corresponding to an intensity of the reflected light, the storage device storing the electric charges, and the first switch thin film transistor receiving the electric charges from the storage device to output the electric charges in response to an external control signal;

forming a first transparent electrode on a lower surface of the first substrate;

forming a second switch thin film transistor on a second substrate comprised of insulation material;

forming a color filter layer on the second switch thin film transistor;

forming a second transparent electrode on the color filter layer;  
aligning the first substrate over the second substrate on a first aspect ratio for a first pixel unit of the first substrate and a second aspect ratio for a second pixel unit of the second substrate;  
and  
forming a liquid crystal layer between the first and second substrates.

16. (Withdrawn) The method of manufacturing the liquid crystal display device of claim 15, wherein the method further comprises forming a first contact hole on the color filter, the first contact hole exposing a first portion of a first electrode of the second switch thin film transistor.

17. (Withdrawn) The method of manufacturing the liquid crystal display device of claim 16, wherein the method further comprises forming a second contact hole on the second transparent electrode, the second contact hold exposing a second portion of the first electrode of the second switch thin film transistor, the second portion corresponding to the first portion of the first electrode of the second switch thin film transistor.

18. (Withdrawn) The method of manufacturing the liquid crystal display device of claim 17, where in the method further comprises forming a first insulation layer between the color filter layer and the second transparent electrode to cover the color filter layer, the first insulation layer having a third contact hole that exposes the second portion of the first electrode of the second switch thin film transistor.

19. (Withdrawn) The method of manufacturing a liquid crystal display device of claim 18, wherein the method further comprises forming a second insulation layer on the sensor thin film transistor, the storage device and the first switch thin film transistor, the second insulation layer covering the sensor thin film transistor, the storage device and the first switch thin film transistor.

20. (Withdrawn) The method of manufacturing a liquid crystal display device of claim 19, wherein the method further comprises:

forming a channel region comprised of amorphous silicon; and  
forming a light shielding layer at a third portion of the second insulation layer, the third portion disposed over the channel region, the light shielding layer shielding the channel region from the light reflected from the fingerprint, and the light shielding layer comprised of Cr/CrxOy.

21. (Withdrawn) The method of manufacturing a liquid crystal display device of claim 15, wherein the method further comprises:

forming a channel region comprised of amorphous silicon; and  
forming a light shielding layer, disposed over the channel region, for shielding the channel region from the light reflected from the fingerprint, and the light shielding layer comprised of Cr/CrxOy.

22. (Withdrawn) The method of manufacturing a liquid crystal display device of claim 15, wherein the method further comprises:

connecting a data reading part to a first side face of the first substrate, the data reading part being disposed adjacent to the first side face of the first substrate and receiving the charges from a first electrode of the first switch thin film transistor to generate a fingerprint identification signal corresponding to the fingerprint;

connecting a first gate driver part to a second side face of the first substrate, the first gate driver part being disposed adjacent to the second side face of the first substrate and turning on or turning off a second electrode of the first switch thin film transistor and a second electrode of the sensor thin film transistor;

connecting a data driver part to a first side face of the second substrate, the data driver part being disposed adjacent to the first side face of the second substrate and applying an image data signal to a third electrode of the second thin film transistor through the data line; and

connecting a second gate driver part to a second side face of the second substrate, the second gate driver part being disposed adjacent to the second side face of the second substrate and turning on or turning off the second electrode of the second switch thin film transistor, wherein the data driver part is opposite to the data reading part and the first gate driver part is opposite to the second gate driver part.

23. (Currently Amended) A liquid crystal display device comprising:  
a first substrate including a transparent substrate, a plurality of unit cells and a first transparent electrode, each of the unit cells having i) a sensor thin film transistor for receiving a light reflected from a fingerprint to generate electric charges corresponding to an intensity of the reflected light, ii) a storage device for storing the electric charges, iii) a first switch thin film transistor for receiving the electric charges from the storage device to output the electric charges in response to an external control signal,

wherein

the transparent substrate includes an upper surface, a lower surface facing the upper surface, and sidewalls connecting the upper and lower surfaces; and

the first transparent electrode is being disposed on the lower surface of the first substrate, the first transparent electrode makes contact with the lower surface of the first substrate, and the unit cells make contact with the upper surface of the lower surface opposing a surface including the unit cells, wherein no additional substrate for forming the first transparent electrode is provided;

a second substrate;

a pixel including i) a data wiring having a data line formed in the second substrate, ii) a color filter layer on the second substrate on which the data wiring is formed, the color filter layer covering a first portion of the data wiring, iii) an insulation layer covering the data wiring and the color filter layer, iv) a second switch thin film transistor formed on the insulation layer, and v) a second transparent electrode electrically coupled with a second portion of a first electrode of the second switch thin film transistor; and

a liquid crystal layer interposed between the first and second substrates, wherein the liquid crystal layer contacts the first transparent electrode disposed on the lower surface of the first substrate.

24. (Previously Presented) The liquid crystal display device of claim 23, wherein three unit cells are arranged over the pixel having a first aspect ratio of 1:3, each of the unit cells having a second aspect ratio of 1:1.



25. (Original) The liquid crystal display device of claim 24, wherein the first substrate further comprises a sensor signal output line for outputting a sensing signal, the sensor signal output line being connected to the sensor thin film transistor, the gate line and a third electrode of the sensor thin film transistor, and the sensor signal output line being comprised of transparent conductive material.

26. (Original) The liquid crystal display device of claim 23, wherein the first substrate further comprises a second insulation layer covering the sensor thin film transistor, the storage device and the first switch thin film transistor.

27. (Original) The liquid crystal display device of claim 26, wherein the first switching thin film transistor comprises:

- a channel region comprised of amorphous silicon; and
- a light shielding layer, disposed on a third portion of the second insulation layer, for shielding the channel region from the light reflected from the fingerprint, the third portion disposed over the channel region, and the light shielding layer comprised of Cr/CrxOy.

28. (Original) The liquid crystal display device of claim 26, wherein the first switching thin film transistor comprises:

- a channel region comprised of amorphous silicon; and
- a light shielding layer, disposed over the channel region, for shielding the channel region from the light reflected from the fingerprint, and the light shielding layer comprised of Cr/CrxOy.

29. (Previously Presented) The liquid crystal display device of claim 23, wherein the data line is aligned with the second switch thin film transistor.